

# Cover Sheet: Request 11291

## EEL4XXX Automated Hardware/Software Verification

### Info

Process	Course New Ugrad/Pro
Status	Pending
Submitter	Chillingworth,Shannon M schill@ece.ufl.edu
Created	11/10/2016 3:24:58 PM
Updated	5/8/2017 1:05:30 PM
Description of request	New course approval.

### Actions

Step	Status	Group	User	Comment	Updated
Department	Approved	ENG - Electrical and Computer Engineering 011905000	Fox, Robert M		11/14/2016
Added 4XXX AutoHwSw - ucc1 syl.docx					11/10/2016
College	Approved	ENG - College of Engineering	Dublin, Heidi Dickerson		4/20/2017
No document changes					
University Curriculum Committee	Comment	PV - University Curriculum Committee (UCC)	Case, Brandon	Added to the May agenda.	4/25/2017
No document changes					
University Curriculum Committee	Pending	PV - University Curriculum Committee (UCC)			4/25/2017
No document changes					
Statewide Course Numbering System					
No document changes					
Office of the Registrar					
No document changes					
Student Academic Support System					
No document changes					
Catalog					
No document changes					
College Notified					
No document changes					

# Course|New for request 11291

## Info

**Request:** EEL4XXX Automated Hardware/Software Verification

**Description of request:** New course approval.

**Submitter:** Chillingworth, Shannon M schill@ece.ufl.edu

**Created:** 11/10/2016 3:24:58 PM

**Form version:** 1

## Responses

**Recommended Prefix**EEL

**Course Level** 4

**Number** XXX

**Category of Instruction** Advanced

**Lab Code** None

**Course Title**Automated Hardware/Software Verification

**Transcript Title**AUTOMATED HW/SW VER.

**Degree Type**Baccalaureate

**Delivery Method(s)**On-Campus

**Co-Listing**Yes

**Co-Listing Explanation**Research paper presentations – Graduates only

Each graduate student will choose a research paper, preferably related to their term project, in the field of automated verification and present it in class. The presentations will be graded based on 1) the presenter's ability to clearly describe the problem, explain the solution, and evaluate the (experimental) results, 2) the quality of answers provided to the questions, and 3) the content of the slides.

Written Questions about Research Papers – Undergraduates only

Each undergraduate student will prepare at least one non-trivial question for 3 papers. To get full credit, the question should reveal that the paper has been read carefully and the answer to the question must not be explicitly stated in the paper. Each question should be submitted prior to the presentation.

Take Home Final – Graduates and Undergraduates

This will include questions on the theory of automated verification as well as research problems that will require a basic understanding of the fundamental problems in the model checking field and various solutions to these problems and the ability to compare and contrast them.

Term Project – Graduates and Undergraduates

The semester long project will involve analysis of a reasonably complex hardware or software system. Students will choose from a list of project topics that will be provided by the instructor.

Extra Credit – Graduates and Undergraduates

The following will be subject to bonus points at the discretion of the instruction: Class participation (attendance as well as interaction), answering bonus questions in class, submitting an assignment on time when the assignment deadline gets extended upon request from the majority of the class, answering bonus assignment questions, finding interesting results as part of the project, e.g., discovering a bug that was not known before. You are encouraged to keep a log of any activity along with the date it happened and other information and submit the bonus log at the end of the semester for extra credit. The log can receive a maximum of 8 points.

**Effective Term** Fall  
**Effective Year** 2017  
**Rotating Topic?** No  
**Repeatable Credit?** No

**Amount of Credit** 3

**S/U Only?** No

**Contact Type** Regularly Scheduled

**Weekly Contact Hours** 003

**Course Description** Develop modeling, formal specification, and automated verification skills for analyzing complex hardware and/or software systems. Hands-on experience with model checking tools.

**Prerequisites** (EEL 3744C or equivalent) and (COP 3530 or equivalent)

**Co-requisites** None

**Rationale and Placement in Curriculum** This technical elective introduces the student to the area of automated verification for analyzing complex hardware/software systems.

**Course Objectives** The student will be able to create formal models for reasonably complex systems to learn various property specification formalisms. The student will be able to use automated verification tools to reason about the correctness properties and the behavior of hardware and/or software systems.

**Course Textbook(s) and/or Other Assigned Reading** No Required Textbooks.

Recommended Reading-  
Research papers provided by instructor.

- a. Title: Theoretical Background
- b. Author: Edmund M. Clarke, Orna Grumberg and Doron A. Peled
- c. Publication date and edition: 1999
- d. ISBN number: 0262032708

- a. Title: The SPIN Model Checker
- b. Author: Gerard J. Holzmann
- c. Publication date and edition: 2003
- d. ISBN number: 0-321-22862-6

**Weekly Schedule of Topics** (1 class) Introduction  
(3 classes) Linear-Time Temporal Logic (LTL)  
(3 classes) SPIN (an explicit-state model checking tool for high-level models)  
(2 classes) Explicit-state Model Checking Algorithms  
(3 classes) Computation Tree Logic (CTL)  
(1 class) NuSMV (a symbolic model checking tool)  
(3 classes) Symbolic Model Checking Algorithms  
(3 classes) Binary Decision Diagrams (BDDs)  
(1 class) Java Path Finder (an explicit-state model checking tool for Java programs)  
(1 class) SAT Solvers  
(1 class) Bounded Model Checking  
(3 classes) Bounded model checkers CBMC (for C and C++ programs), VIS and EBMC (for Verilog models)  
(2 classes) Timed Automata and UPPAAL model checker  
(2 classes) Probabilistic Model Checking and PRISM model checker  
(2 classes) Hybrid Automata and CheckMate model checker  
(3 classes) CEGAR approach and SATABS and VCEGAR tools  
(~7 classes) Research Paper presentations  
(1 class) Wrap-up

**Links and Policies** Attendance and Expectations - This course is co-listed with the graduate class. Cell phones and other electronic devices are to be silenced. No text messaging during class or exams.

Requirements for class attendance and make-up exams, assignments, and other work in this course are consistent with university policies that can be found in the online catalog at: <https://catalog.ufl.edu/ugrad/current/regulations/info/attendance.aspx>

16. Grading Scale –

A	A-	B+	B	B-	C+	C	C-	D+	D	D-
	E									
93-100		90-92	87-89	83-86	80-82	77-79	73-76	70-72	67-69	63-66
	60-62	0-59								

"A C- will not be a qualifying grade for critical tracking courses. In order to graduate, students must have an overall GPA and an upper-division GPA of 2.0 or better (C or better)." Note: a C- average is equivalent to a GPA of 1.67, and therefore, it does not satisfy this graduation requirement. For more information on grades and grading policies, please visit: <https://catalog.ufl.edu/ugrad/current/regulations/info/grades.aspx>

Make-Up Exam Policy - If you have a University-approved excuse and arrange for it in advance, or in case of documented emergency, a make-up exam will be allowed and arrangements can be made for making up missed work. University attendance policies can be found at: <https://catalog.ufl.edu/ugrad/current/regulations/info/attendance.aspx>

Otherwise, make-up exams will be considered only in extraordinary cases, and must be taken before the scheduled exam. The student must submit a written petition to the instructor two weeks prior to the scheduled exam and the instructor must approve the petition.

Honesty Policy – UF students are bound by The Honor Pledge which states, "We, the members of the University of Florida community, pledge to hold ourselves and our peers to the highest standards of honor and integrity by abiding by the Honor Code. On all work submitted for credit by students at the University of Florida, the following pledge is either required or implied: "On my honor, I have neither given nor received unauthorized aid in doing this assignment." The Honor Code (<http://www.dso.ufl.edu/sccr/process/student-conduct-honor-code/>) specifies a number of behaviors that are in violation of this code and the possible sanctions. Furthermore, you are obligated to report any condition that facilitates academic misconduct to appropriate personnel. If you have any questions or concerns, please consult with the instructor or TAs in this class.

Accommodation for Students with Disabilities – Students requesting classroom accommodation must first register with the Dean of Students Office. That office will provide documentation to the student who must then provide this documentation to the course instructor when requesting accommodation.

UF Counseling Services – Resources are available on-campus for students having personal problems or lacking clear career and academic goals. The resources include:

- UF Counseling & Wellness Center, psychological and psychiatric services, 3190 Radio Rd, 392-1575, online: <http://www.counseling.ufl.edu/cwc/Default.aspx>,
- Career Resource Center, Reitz Union, career and job search services, 392-1601.
- University Police Department, 392-1111 or 911 for emergencies

Software Use – All faculty, staff and student of the University are required and expected to obey the laws and legal agreements governing software use. Failure to do so can lead to monetary damages and/or criminal penalties for the individual violator. Because such violations are also against University policies and rules, disciplinary action will be taken as appropriate. We, the members of the University of Florida community, pledge to uphold ourselves and our peers to the highest standards of honesty and integrity.

Course Evaluation – Students are expected to provide feedback on the quality of instruction in this course based on 10 criteria. These evaluations are conducted online at: <https://evaluations.ufl.edu>. Evaluations are typically open during the last two or three weeks of the semester, but students will be given specific times when they are open. Summary results of these assessments are available to students at: <https://evaluations.ufl.edu/results>.

**Grading Scheme** Grading –

30% Written assignments and hands-on experience with model checking tools  
25% Take home final exam  
15% Written Questions about Research Papers  
30% Term project

Research paper presentations – Graduates only

Each graduate student will choose a research paper, preferably related to their term project, in the field of automated verification and present it in class. The presentations will be graded based on 1) the presenter's ability to clearly describe the problem, explain the solution, and evaluate the (experimental) results, 2) the quality of answers provided to the questions, and 3) the content of the slides.

Written Questions about Research Papers – Undergraduates only

Each undergraduate student will prepare at least one non-trivial question for 3 papers. To get full credit, the question should reveal that the paper has been read carefully and the answer to the question must not be explicitly stated in the paper. Each question should be submitted prior to the presentation.

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Term Project – Graduates and Undergraduates

The semester long project will involve analysis of a reasonably complex hardware or software system. Students will choose from a list of project topics that will be provided by the instructor.

Extra Credit – Graduates and Undergraduates

The following will be subject to bonus points at the discretion of the instructor: Class participation (attendance as well as interaction), answering bonus questions in class, submitting an assignment on time when the assignment deadline gets extended upon request from the majority of the class, answering bonus assignment questions, finding interesting results as part of the project, e.g., discovering a bug that was not known before. You are encouraged to keep a log of any activity along with the date it happened and other information and submit the bonus log at the end of the semester for extra credit. The log can receive a maximum of 8 points.

**Instructor(s)** Dr. Tuba Yavuz

EEL 4XXX Automated Hardware/Software Verification

1. Catalog Description – (3 credits) Develop modeling, formal specification, and automated verification skills for analyzing complex hardware and/or software systems. Hands-on experience with model checking tools.
2. Pre-requisites – (EEL 3744C or equivalent) and (COP 3530 or equivalent)
3. Course Objectives – The student will be able to create formal models for reasonably complex systems to learn various property specification formalisms. The student will be able to use automated verification tools to reason about the correctness properties and the behavior of hardware and/or software systems.
4. Contribution of course to meeting the professional component (ABET only – undergraduate courses) – 3 hours of Engineering Science
5. Relationship of course to program outcomes (ABET only undergraduate courses) – a, k
6. Instructor – Dr. Tuba Yavuz
  - a. Office location: 321 Benton Hall
  - b. Telephone: 352-846-0202
  - c. E-mail address: [tuba@ece.ufl.edu](mailto:tuba@ece.ufl.edu)
  - d. Class Web site: <http://lss.at.ufl.edu> (Canvas)
  - e. Office hours: Mondays, 3<sup>rd</sup> period; Thursdays 10:00 a.m.-11:00 a.m.
7. Teaching Assistant - None
8. Meeting Times and Location – Monday, Wednesday, Friday 11:45 a.m. – 12:35 p.m., 310 Larsen
9. Class/laboratory schedule - 3 class periods each week consisting of 50 minutes each
10. Material and Supply Fees - None
11. Textbooks and Software Required - None
12. Recommended Reading – Research papers provided by instructor.
  - a. Title: Theoretical Background
  - b. Author: Edmund M. Clarke, Orna Grumberg and Doron A. Peled
  - c. Publication date and edition: 1999
  - d. ISBN number: 0262032708
  
  - a. Title: The SPIN Model Checker
  - b. Author: Gerard J. Holzmann
  - c. Publication date and edition: 2003

d. ISBN number: 0-321-22862-6

13. Course Outline (provide topics covered by week or by class period) –

- (1 class) Introduction
- (3 classes) Linear-Time Temporal Logic (LTL)
- (3 classes) SPIN (an explicit-state model checking tool for high-level models)
- (2 classes) Explicit-state Model Checking Algorithms
- (3 classes) Computation Tree Logic (CTL)
- (1 class) NuSMV (a symbolic model checking tool)
- (3 classes) Symbolic Model Checking Algorithms
- (3 classes) Binary Decision Diagrams (BDDs)
- (1 class) Java Path Finder (an explicit-state model checking tool for Java programs)
- (1 class) SAT Solvers
- (1 class) Bounded Model Checking
- (3 classes) Bounded model checkers CBMC (for C and C++ programs), VIS and EBMC (for Verilog models)
- (2 classes) Timed Automata and UPPAAL model checker
- (2 classes) Probabilistic Model Checking and PRISM model checker
- (2 classes) Hybrid Automata and CheckMate model checker
- (3 classes) CEGAR approach and SATABS and VCEGAR tools
- (~7 classes) Research Paper presentations
- (1 class) Wrap-up

14. Attendance and Expectations - This course is co-listed with the graduate class. Cell phones and other electronic devices are to be silenced. No text messaging during class or exams.

Requirements for class attendance and make-up exams, assignments, and other work in this course are consistent with university policies that can be found in the online catalog at: <https://catalog.ufl.edu/ugrad/current/regulations/info/attendance.aspx>

15. Grading –

- 30% Written assignments and hands-on experience with model checking tools
- 25% Take home final exam
- 15% Written Questions about Research Papers
- 30% Term project

Research paper presentations – Graduates only

Each graduate student will choose a research paper, preferably related to their term project, in the field of automated verification and present it in class. The presentations will be graded based on 1) the presenter's ability to clearly describe the problem, explain the solution, and evaluate the (experimental) results, 2) the quality of answers provided to the questions, and 3) the content of the slides.

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Each undergraduate student will prepare at least one non-trivial question for 3 papers. To get full credit, the question should reveal that the paper has been read carefully and the

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by students at the University of Florida, the following pledge is either required or implied: “On my honor, I have neither given nor received unauthorized aid in doing this assignment.” The Honor Code (<http://www.dso.ufl.edu/sccr/process/student-conduct-honor-code/>) specifies a number of behaviors that are in violation of this code and the possible sanctions. Furthermore, you are obligated to report any condition that facilitates academic misconduct to appropriate personnel. If you have any questions or concerns, please consult with the instructor or TAs in this class.

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EEL 5XXX Automated Hardware/Software Verification

1. Catalog Description – (3 credits) Develop modeling, formal specification, and automated verification skills for analyzing complex hardware and/or software systems. Hands-on experience with model checking tools.
2. Pre-requisites – Data Structures, Algorithms and Architecture
3. Course Objectives – The student will be able to create formal models for reasonably complex systems to learn various property specification formalisms. The student will be able to use automated verification tools to reason about the correctness properties and the behavior of hardware and/or software systems.
4. Contribution of course to meeting the professional component (ABET only – undergraduate courses) – 3 hours of Engineering Science
5. Relationship of course to program outcomes (ABET only undergraduate courses) – a, k
6. Instructor – Dr. Tuba Yavuz
  - a. Office location: 321 Benton Hall
  - b. Telephone: 352-846-0202
  - c. E-mail address: [tuba@ece.ufl.edu](mailto:tuba@ece.ufl.edu)
  - d. Class Web site: <http://lss.at.ufl.edu> (Canvas)
  - e. Office hours: Mondays, 3<sup>rd</sup> period; Thursdays 10:00 a.m.-11:00 a.m.
7. Teaching Assistant - None
8. Meeting Times and Location – Monday, Wednesday, Friday 11:45 a.m. – 12:35 p.m., 310 Larsen
9. Class/laboratory schedule - 3 class periods each week consisting of 50 minutes each
10. Material and Supply Fees - None
11. Textbooks and Software Required - None
12. Recommended Reading – Research papers provided by instructor.
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  - c. Publication date and edition: 1999
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